AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning at page 12, line 3 as follows:

After the sequencer 325 is invoked, it switches a plurality of gates 316-320 prior to the execution ~~queues~~ units 301-305 for the primary instruction form, de-gating the primary instruction form. In addition, the branch unit 305 signals the fetch unit 322 to stop fetching instructions of the primary form from memory 321. The sequencer 325 includes an alternate program counter for directing the fetching of the decoded instructions. Further, the sequencer 325 sequences of the decoded instructions (control signals) from the local predecoded instruction buffers. Individual program counters can be implemented for each buffer to improve the efficiency with which the buffer space is used.